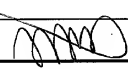


1657 U.S. PTO
09/634043
03/24/00

UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	0057-2608-2YY
	First Inventor or Application Identifier	Toshiaki SHINOHARA, et al.
	Title	SEMICONDUCTOR MODULE AND INSULATING SUBSTRATE THEREOF

APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents</small>	ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) <small>(Submit an original and a duplicate for fee processing)</small> 2. <input checked="" type="checkbox"/> Specification Total Pages 21 3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets 5 4. <input checked="" type="checkbox"/> Oath or Declaration Total Pages 4 a. <input checked="" type="checkbox"/> Newly executed (original) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) <small>(for continuation/divisional with box 15 completed)</small> i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 5. <input type="checkbox"/> Incorporation By Reference <small>(usable if box 4B is checked)</small> The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4B, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein.	ACCOMPANYING APPLICATION PARTS 6. <input type="checkbox"/> Assignment Papers (cover sheet & document(s)) 7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney <small>(when there is an assignee)</small> 8. <input type="checkbox"/> English Translation Document <small>(if applicable)</small> 9. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations 10. <input type="checkbox"/> Preliminary Amendment 11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard 12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application, Status still proper and desired. 13. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (1) <small>(if foreign priority is claimed)</small> 14. <input checked="" type="checkbox"/> Other: Notice of Priority
15. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: Prior application information: Examiner: Group Art Unit:	
16. Amend the specification by inserting before the first line the sentence: <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on <input type="checkbox"/> This application claims priority of provisional application Serial No. Filed	
17. CORRESPONDENCE ADDRESS OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. FOURTH FLOOR 1755 JEFFERSON DAVIS HIGHWAY ARLINGTON, VIRGINIA 22202 (703) 413-3000 FACSIMILE: (703) 413-2220	

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Signature:		Date:	3/24/00
Name:		Registration No.:	

TITLE OF THE INVENTION

SEMICONDUCTOR MODULE AND INSULATING SUBSTRATE
THEREFOR

5 BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a power semiconductor module with a semiconductor device such as a power device mounted therein and, more particularly, to a structure of a mounting frame for forcing the semiconductor module into pressure contact
10 with an external heat sink.

Description of the Background Art

Fig. 9 is a cross-sectional view of a background art semiconductor module 118 shown as mounted on an external heat sink 111. The semiconductor module 118
15 comprises: an insulating substrate 117 including a ceramic plate 101, a first metal plate 102 and a second metal plate 103; a semiconductor device 105, such as a power device, mounted on the first metal plate 102 with solder 106; a case 104 having inside electrodes 109 connected through wires 107 to the semiconductor device 105 or the first metal plate 102, the case 104 being made of a thermoplastic resin such as polyphenylene sulfide
20 (PPS) and polybutylene terephthalate (PBT); a resin 108 enclosed and cured in the case 104; and a cover 110 for the case 104.

After silicone grease is applied to a surface of the second metal plate 103 for enhancement of a heat dissipating property, the case 104 and the external heat sink 111 are fastened by screws 112, whereby the pressing force of the case 104 brings the
25 insulating substrate 117 into pressure contact with the external heat sink 111.

In such a background art semiconductor module, the insulating substrate is forced into pressure contact with the external heat sink by the pressing force of the case, and the case is made of a thermoplastic resin. Thus, the case is gradually deformed by creep when used, in particular, continually in a high-temperature environment, and the pressing force of the case exerted upon the insulating substrate decreases with time. As a result, poor contact between the second metal plate and the external heat sink degrades the heat dissipating property.

SUMMARY OF THE INVENTION

A first aspect of the present invention is intended for a semiconductor module mountable on an external heat sink. According to the present invention, the semiconductor module comprises: an insulating substrate for the semiconductor module, the insulating substrate including a substrate, a first conductive pattern formed on a first main surface of the substrate which is on the opposite side from the external heat sink, and a second conductive pattern formed on a second main surface of the substrate which is on the same side as the external heat sink and for contact with the external heat sink; and a mounting frame made of metal and having a mounting surface for contact with the external heat sink, the mounting frame including a flange along a periphery thereof for engagement with a peripheral part of the insulating substrate at the first main surface, the flange pressing the peripheral part of the insulating substrate toward the external heat sink to force the insulating substrate into pressure contact with the external heat sink.

Preferably, according to a second aspect of the present invention, in the semiconductor module of the first aspect, the mounting frame further includes: a first metal plate having the mounting surface; and a second metal plate disposed on the first metal plate and having a protrusion along a periphery thereof projecting from a periphery

of the first metal plate to define the flange.

Preferably, according to a third aspect of the present invention, in the semiconductor module of the second aspect, the thickness of the first metal plate is equal to the sum of the thickness of the substrate and the thickness of the second conductive pattern. The thickness of the second metal plate is equal to the thickness of the first metal plate.

Preferably, according to a fourth aspect of the present invention, in the semiconductor module of any one of the first to third aspects, the insulating substrate further includes a third conductive pattern formed on the first main surface along a periphery of the substrate. The flange and the insulating substrate contact each other, with the third conductive pattern therebetween.

Preferably, according to a fifth aspect of the present invention, in the semiconductor module of the fourth aspect, the third conductive pattern is formed partially to allow part of the flange to contact the third conductive pattern. The mounting frame and the insulating substrate are bonded to each other with an adhesive filling a gap between part of the flange which is out of contact with the third conductive pattern and the first main surface.

Preferably, according to a sixth aspect of the present invention, in the semiconductor module of any one of the first to third aspects, the substrate, the first conductive pattern and the second conductive pattern of the insulating substrate have respective peripheries in alignment with each other. The flange presses the periphery of the first conductive pattern toward the external heat sink, with an insulative material between the flange and the first conductive pattern.

Preferably, according to a seventh aspect of the present invention, the semiconductor module of any one of the first to sixth aspects further comprises: a

semiconductor device mounted on the first conductive pattern; a cylindrical case disposed on a main surface of the mounting frame which is on the opposite side from the external heat sink; the case, the mounting frame and the insulating substrate defining a space surrounding the semiconductor device; and an insulative sealing material filling the space.

Preferably, according to an eighth aspect of the present invention, in the semiconductor module of the seventh aspect, the sealing material is a thermosetting resin.

A ninth aspect of the present invention is intended for an insulating substrate for a semiconductor module. According to the present invention, the insulating substrate comprises a mounting surface, the mounting surface being adapted to be forced into pressure contact with an external heat sink by a mounting frame pressing a peripheral part of the insulating substrate, the insulating substrate having a curved configuration in which a peripheral part of the mounting surface warps upwardly away from the external heat sink above a central part of the mounting surface.

In accordance with the first aspect of the present invention, the insulating substrate for the semiconductor module is forced into pressure contact with the external heat sink by the pressing force of the metal mounting frame, rather than a conventional case made of a thermoplastic resin. This causes no creep, to avoid the problem of a decreasing pressing force resulting from the deformation of the frame even after continual use in a high-temperature environment. Consequently, the semiconductor module can ensure a satisfactory heat dissipating property over a long period of time.

Additionally, the metal frame has a better heat dissipating property than the conventional case made of the thermoplastic resin. This achieves the size reduction of the semiconductor module.

The production of the flange of the mounting frame by pressing requires

various types of manufacturing management such as the management of a pressing tolerance and the management of the flatness of the mounting frame. On the other hand, in the case of the semiconductor module in accordance with the second aspect of the present invention, the management of only the tolerance of the thickness of the first metal plate is required. This facilitates the manufacturing management.

Further, the semiconductor module in accordance with the second aspect of the present invention eliminates the need to machine the flange by pressing to achieve the increase in productivity and the reduction in costs.

In accordance with the third aspect of the present invention, the thickness of the first metal plate is equal to the thickness of the second metal plate. This further facilitates the manufacturing management.

In accordance with the fourth aspect of the present invention, the flange and the insulating substrate contact each other, with the third conductive pattern therebetween. This provides further enhancement of the heat dissipating property and further size reduction of the semiconductor module.

Additionally, the semiconductor module in accordance with the fourth aspect of the present invention can insure uniform stresses applied from the flange through the third conductive pattern to the substrate, suitably preventing cracking in the substrate.

In accordance with the fifth aspect of the present invention, the adhesive may be made as thick as the third conductive pattern. Thus, if a resin in gel form is used as a sealing material for covering a semiconductor device, the adhesive prevents the sealing material from flowing out, providing the highly reliable semiconductor module.

In accordance with the sixth aspect of the present invention, the size of the insulating substrate is reduced in accordance with the elimination of an overhang extended outwardly from the periphery of the first conductive pattern. The size of the

semiconductor module itself is accordingly reduced.

In accordance with the seventh aspect of the present invention, the insulative sealing material filling the space provides a mechanical strength and ensures insulation between the metal mounting frame and the semiconductor device.

5 In accordance with the eighth aspect of the present invention, the insulating substrate is prevented from being broken by an external force applied from the direction of the second conductive pattern, before the semiconductor module is mounted on the external heat sink.

10 In accordance with the ninth aspect of the present invention, when the peripheral part of the mounting surface is forced into pressure contact with the external heat sink by the mounting frame for the mounting of the insulating substrate on the external heat sink, the central part of the mounting surface is also necessarily brought into pressure contact with the external heat sink. Thus, the ninth aspect of the present invention ensures better contact between the insulating substrate and the external heat
15 sink particularly at the central part of the mounting surface, accomplishing further enhancement of the heat dissipating property.

It is therefore an object of the present invention to provide a semiconductor module and an insulating substrate therefor which can avoid the problem of a decreasing pressing force resulting from deformation to ensure a satisfactory heat dissipating
20 property over a long period of time.

These and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

25 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a cross-sectional view of a semiconductor module shown as mounted on an external heat sink according to a first preferred embodiment of the present invention;

5 Fig. 2 is a cross-sectional view of the semiconductor module shown as mounted on the external heat sink according to a second preferred embodiment of the present invention;

Fig. 3 is an enlarged cross-sectional view of part of a structure of the semiconductor module shown as mounted on the external heat sink according to a third preferred embodiment of the present invention;

10 Fig. 4 is an enlarged cross-sectional view of part of another structure of the semiconductor module shown as mounted on the external heat sink according to the third preferred embodiment of the present invention;

15 Fig. 5 is a cross-sectional view of the semiconductor module shown as mounted on the external heat sink according to a fourth preferred embodiment of the present invention;

Fig. 6 is a cross-sectional view of a structure of an insulating substrate for the semiconductor module according to a fifth preferred embodiment of the present invention;

20 Fig. 7 is a cross-sectional view of a structure of the insulating substrate for the semiconductor module according to a first modification of the fifth preferred embodiment of the present invention;

Fig. 8 is a cross-sectional view of a structure of the insulating substrate for the semiconductor module according to a second modification of the fifth preferred embodiment of the present invention; and

25 Fig. 9 is a cross-sectional view of a background art semiconductor module

shown as mounted on an external heat sink.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

First Preferred Embodiment

5 Fig. 1 is a cross-sectional view of a semiconductor module 18 shown as mounted on an external heat sink 11 according to a first preferred embodiment of the present invention. The semiconductor module 18 comprises an insulating substrate 17 for a semiconductor module. The insulating substrate 17 includes a ceramic plate 1, a first metal plate 2 formed on a first main surface of the ceramic plate 1 which is on the
10 opposite side from the external heat sink 11, and a second metal plate 3 formed on a second main surface of the ceramic plate 1 which is on the same side as the external heat sink 11 and for contact with the external heat sink 11.

The semiconductor module 18 further comprises a ring-shaped metal frame 13 having a bottom surface for contact with the top surface of the external heat sink 11 and
15 serving as a mounting surface. The ring-shaped metal frame 13 has a flange 20 along an inner periphery thereof for engagement with an outer peripheral part of the insulating substrate 17 at the first main surface of the ceramic plate 1. The metal frame 13 is fastened to the external heat sink 11 by screws 12 or bonded to the external heat sink 11 with an adhesive. The flange 20 of the metal frame 13 fastened or bonded to the
20 external heat sink 11 presses the outer peripheral part of the insulating substrate 17 toward the external heat sink 11. This pressing force holds the insulating substrate 17 in pressure contact with the external heat sink 11.

The semiconductor module 18 further comprises a semiconductor device 5, such as a power device, mounted on the first metal plate 2 with solder 6, and a cylindrical
25 hollow case 4 bonded to or fastened by screws to a main surface of the metal frame 13

which is on the opposite side from the external heat sink 11. The case 4 is made of a thermoplastic resin such as PPS and PBT, and electrodes 9 are disposed inside the case 4. The electrodes 9 are connected to the semiconductor device 5 or the first metal plate 2 through wires 7 such as aluminum fine lines.

5 The side surface of the case 4, the side surface of the flange 20 of the metal frame 13 and the top surface of the insulating substrate 17 define a space surrounding the semiconductor device 5. This space is filled with an insulative sealing material 8 which covers at least the semiconductor device 5 and the wires 7. This provides a mechanical strength and ensures insulation between the metal frame 13, the semiconductor device 5
10 and the wires 7. An example of the sealing material 8 used herein may include a thermosetting resin such as epoxy-based resin. Alternatively, silicone gel may be used as the sealing material 8 when the mechanical strength is sufficiently high. A cover 10 is bonded to or fastened by screws to the case 4, as required.

In the semiconductor module 18 according to the first preferred embodiment of
15 the present invention, as described hereinabove, the insulating substrate 17 is forced into pressure contact with the external heat sink 11 by the pressing force of the metal frame 13, rather than the background art case 104 made of the thermoplastic resin. This causes no creep, to avoid the problem of the decreasing pressing force resulting from the deformation of the metal frame 13 even after continual use in a high-temperature
20 environment. Consequently, the semiconductor module 18 can ensure a satisfactory heat dissipating property over a long period of time.

Additionally, the metal frame 13 has a better heat dissipating property than the background art case 104 made of the thermoplastic resin. This achieves the size reduction of the semiconductor module.

25 Furthermore, when the thermosetting resin is used as the sealing material 8, the

insulating substrate 17 is prevented from being broken by an external force applied from the direction of the second metal plate 3 before the semiconductor module 18 is mounted on the external heat sink 11.

5 Second Preferred Embodiment

Fig. 2 is a cross-sectional view of a semiconductor module 19 shown as mounted on the external heat sink 11 according to a second preferred embodiment of the present invention. The semiconductor module 19 comprises a double-layer structure including a lower metal plate 13a having a mounting surface for contact with the external
10 heat sink 11 and an upper metal plate 13b secured on the lower metal plate 13a, in place of the metal frame 13 shown in Fig. 1. The inner periphery of the upper metal plate 13b projects inwardly from the inner periphery of the lower metal plate 13a to define a protrusion 21 corresponding to the flange 20 shown in Fig. 1.

The thickness of the lower metal plate 13a is equal to the sum of the thickness
15 of the ceramic plate 1 and the thickness of the second metal plate 3. The upper metal plate 13b may be of any thickness, but is preferably as thick as the lower metal plate 13a in consideration for the ease of manufacturing management. The remaining structure of the semiconductor module 19 according to the second preferred embodiment is identical with the corresponding structure of the semiconductor module 18 of the first preferred
20 embodiment shown in Fig. 1.

As described hereinabove, the semiconductor module 19 according to the second preferred embodiment comprises the double-layer structure including the lower metal plate 13a and the upper metal plate 13b, in place of the metal frame 13 shown in Fig. 1. The production of the flange 20 of the metal frame 13 shown in Fig. 1 by
25 pressing requires various types of manufacturing management such as the management of

a pressing tolerance and the management of the flatness of the metal frame 13. On the other hand, in the case of the semiconductor module 19 according to the second preferred embodiment, the management of only the tolerance of the thickness of the lower metal plate 13a is required. This facilitates the manufacturing management.

- 5 Further, the second preferred embodiment eliminates the need to machine the flange 20 by pressing to achieve the increase in productivity and the reduction in costs.

Third Preferred Embodiment

- Fig. 3 is an enlarged cross-sectional view of part of a structure of the semiconductor module shown as mounted on the external heat sink 11 according to a
10 third preferred embodiment of the present invention. The semiconductor module according to the third preferred embodiment shown in Fig. 3 comprises a third metal plate 25 formed on the first main surface of the ceramic plate 1 along the outer periphery of the ceramic plate 1. The flange 20 of the metal frame 13 and the ceramic plate 1 are in
15 contact with each other, with the third metal plate 25 therebetween.

The third metal plate 25 is formed partially so that part of the flange 20 contacts the third metal plate 25. The metal frame 13 and the insulating substrate 17 are bonded to each other with an adhesive 14 filling the gap between the flange 20 and the ceramic plate 1.

- 20 Although the third preferred embodiment of the present invention is applied to the semiconductor module 18 of the first preferred embodiment in the above description, the third preferred embodiment may be applied to the semiconductor module 19 of the second preferred embodiment, as illustrated in Fig. 4. The remaining structure of the semiconductor module according to the third preferred embodiment is identical with the
25 corresponding structure of the semiconductor modules 18 and 19.

In the semiconductor module according to the third preferred embodiment, as described hereinabove, the metal frame 13 or the upper metal plate 13b and the ceramic plate 1 are in contact with each other, with the third metal plate 25 therebetween. This provides further enhancement of the heat dissipating property and further size reduction of the semiconductor module.

Additionally, the third preferred embodiment can insure uniform stresses applied from the flange 20 or the protrusion 21 through the third metal plate 25 to the ceramic plate 1, suitably preventing cracking in the ceramic plate 1.

Further, the adhesive 14 may be made as thick as the third metal plate 25. Thus, if a resin in gel form is used as the sealing material 8, the adhesive 14 prevents the sealing material 8 from flowing out, providing the highly reliable semiconductor module.

Fourth Preferred Embodiment

Fig. 5 is a cross-sectional view of a semiconductor module 18a shown as mounted on the external heat sink 11 according to a fourth preferred embodiment of the present invention. In the semiconductor module 18 shown in Fig. 1, for instance, the ceramic plate 1 has a peripheral overhang extended outwardly from the outer periphery of the first metal plate 2 so as to prevent the contact between the flange 20 of the metal frame 13 and the first metal plate 2 of the insulating substrate 17. In the semiconductor module 18a according to the fourth preferred embodiment, on the other hand, the peripheral overhang of the ceramic plate 1 is dispensed with so that the outer peripheries of the first metal plate 2 and the ceramic plate 1 are in alignment with each other. Additionally, the outer periphery of the second metal plate 3 is aligned with the outer periphery of the ceramic plate 1.

To prevent the metal frame 13 from contacting the first and second metal plates

2 and 3, a gap 40 is produced between the metal frame 13 and an insulating substrate 17a so that the insulative sealing material 8 flows also into the gap 40. As a result, the flange 20 of the metal frame 13 fastened by the screws to the external heat sink 11 presses the outer peripheral part of the first metal plate 2 toward the external heat sink 11, with the sealing material 8 between the flange 20 and the first metal plate 2. This pressing force holds the insulating substrate 17a in pressure contact with the external heat sink 11.

Although the fourth preferred embodiment of the present invention is applied to the semiconductor module 18 of the first preferred embodiment in the above description, the fourth preferred embodiment may be applied to the semiconductor module 19 of the second preferred embodiment.

In the semiconductor module 18a according to the fourth preferred embodiment, as described hereinabove, the outer peripheries of the ceramic plate 1 and the second metal plate 3 are aligned alignment with the outer periphery of the first metal plate 2. This reduces the size of the insulating substrate 17a in accordance with the elimination of the peripheral overhang, to accordingly reduce the width W of the semiconductor module 18a itself.

Fifth Preferred Embodiment

Fig. 6 is a cross-sectional view of a structure of an insulating substrate 15 for a semiconductor module according to a fifth preferred embodiment of the present invention. The ceramic plate 1, the first metal plate 2 and the second metal plate 3 of the insulating substrate 15 having the structure based on the insulating substrate 17 shown in Figs. 1 and 2 are of a curved configuration such that the outer peripheral part of the mounting surface for contact with the external heat sink 11 warps upwardly away from the external heat sink 11 a distance L (ranging from 0 to 300 μm) above the central part thereof.

Fig. 7 is a cross-sectional view of a structure of an insulating substrate 16 for a semiconductor module according to a modification of the fifth preferred embodiment of the present invention. The ceramic plate 1, the first metal plate 2, the second metal plate 3 and the third metal plate 25 of the insulating substrate 16 having the structure based on the insulating substrate 17 shown in Figs. 3 and 4 are of a curved configuration such that the outer peripheral part of the mounting surface warps upwardly away from the external heat sink 11 in a manner similar to the insulating substrate 15.

The ceramic plate 1, the first metal plate 2, the second metal plate 3 and the third metal plate 25 are illustrated as curved together in the above description. Alternatively, an insulating substrate 41 as shown in Fig. 8 may be provided in which only the bottom surface of the second metal plate 3 is curved convexedly. Furthermore, the fifth preferred embodiment according to the present invention may be applied to the insulating substrate 17a of the fourth preferred embodiment.

As described hereinabove, the insulating substrates 15, 16 and 41 according to the fifth preferred embodiment are shaped such that the outer peripheral part of the mounting surface warps upwardly away from the external heat sink 11 above the central part thereof, before the mounting on the external heat sink 11. When the outer peripheral part of the mounting surface is forced into pressure contact with the external heat sink 11 by the pressing force of the flange 20 or the protrusion 21 for the mounting of the semiconductor module on the external heat sink 11, the central part of the mounting surface is also necessarily brought into pressure contact with the external heat sink 11. Thus, the insulating substrates 15, 16 and 41 according to the fifth preferred embodiment ensure better contact with the external heat sink 11 particularly at the central part of the mounting surface than the insulating substrates 17 and 17a according to the first to fourth preferred embodiments, accomplishing further enhancement of the heat

dissipating property.

While the invention has been described in detail, the foregoing description is in all aspects illustrative and not restrictive. It is understood that numerous other modifications and variations can be devised without departing from the scope of the

5 invention.

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What is claimed is:

1. A semiconductor module mountable on an external heat sink, said semiconductor module comprising:

5 an insulating substrate for said semiconductor module, said insulating substrate including a substrate, a first conductive pattern formed on a first main surface of said substrate which is on the opposite side from said external heat sink, and a second conductive pattern formed on a second main surface of said substrate which is on the same side as said external heat sink and for contact with said external heat sink; and

10 a mounting frame made of metal and having a mounting surface for contact with said external heat sink, said mounting frame including a flange along a periphery thereof for engagement with a peripheral part of said insulating substrate at said first main surface, said flange pressing said peripheral part of said insulating substrate toward said external heat sink to force said insulating substrate into pressure contact with said
15 external heat sink.

2. The semiconductor module according to claim 1,

wherein said mounting frame further includes:

a first metal plate having said mounting surface; and

20 a second metal plate disposed on said first metal plate and having a protrusion along a periphery thereof projecting from a periphery of said first metal plate to define said flange.

3. The semiconductor module according to claim 2,

25 wherein the thickness of said first metal plate is equal to the sum of the

thickness of said substrate and the thickness of said second conductive pattern; and

wherein the thickness of said second metal plate is equal to the thickness of said first metal plate.

5 4. The semiconductor module according to claim 2,

wherein said insulating substrate further includes a third conductive pattern formed on said first main surface along a periphery of said substrate; and

wherein said flange and said insulating substrate contact each other, with said third conductive pattern therebetween.

10

5. The semiconductor module according to claim 4,

wherein said third conductive pattern is formed partially to allow part of said flange to contact said third conductive pattern; and

wherein said mounting frame and said insulating substrate are bonded to each other with an adhesive filling a gap between part of said flange which is out of contact with said third conductive pattern and said first main surface.

15

6. The semiconductor module according to claim 1,

wherein said insulating substrate further includes a third conductive pattern formed on said first main surface along a periphery of said substrate; and

20

wherein said flange and said insulating substrate contact each other, with said third conductive pattern therebetween.

7. The semiconductor module according to claim 6,

25

wherein said third conductive pattern is formed partially to allow part of said

flange to contact said third conductive pattern; and

wherein said mounting frame and said insulating substrate are bonded to each other with an adhesive filling a gap between part of said flange which is out of contact with said third conductive pattern and said first main surface.

5

8. The semiconductor module according to claim 1,

wherein said substrate, said first conductive pattern and said second conductive pattern of said insulating substrate have respective peripheries in alignment with each other; and

10

wherein said flange presses said periphery of said first conductive pattern toward said external heat sink, with an insulative material between said flange and said first conductive pattern.

15

9. The semiconductor module according to claim 1, further comprising:

a semiconductor device mounted on said first conductive pattern;

a cylindrical case disposed on a main surface of said mounting frame which is on the opposite side from said external heat sink;

said case, said mounting frame and said insulating substrate defining a space surrounding said semiconductor device; and

20

an insulative sealing material filling said space.

10. The semiconductor module according to claim 9,

wherein said sealing material is a thermosetting resin.

25

11. An insulating substrate for a semiconductor module, said insulating

substrate comprising a mounting surface, said mounting surface being adapted to be forced into pressure contact with an external heat sink by a mounting frame pressing a peripheral part of said insulating substrate,

5 said insulating substrate having a curved configuration in which a peripheral part of said mounting surface warps upwardly away from said external heat sink above a central part of said mounting surface.

12. The insulating substrate according to claim 11, further comprising:

a substrate;

10 a first conductive pattern formed on a first main surface of said substrate which is on the opposite side from said external heat sink; and

a second conductive pattern formed on a second main surface of said substrate which is on the same side as said external heat sink and having a bottom surface serving as said mounting surface,

15 wherein all of said substrate, said first conductive pattern and said second conductive pattern are curved.

13. The insulating substrate according to claim 11, further comprising:

a substrate;

20 a first conductive pattern formed on a first main surface of said substrate which is on the opposite side from said external heat sink; and

a second conductive pattern formed on a second main surface of said substrate which is on the same side as said external heat sink and having a bottom surface serving as said mounting surface,

25 wherein only the bottom surface of said second conductive pattern is curved

ABSTRACT OF THE DISCLOSURE

A semiconductor module (18) includes a ring-shaped metal frame (13) having a bottom surface for contact with a top surface of an external heat sink (11) and serving as a mounting surface. The ring-shaped metal frame (13) has a flange (20) along an inner periphery thereof for engagement with an outer peripheral part of an insulating substrate (17) at a first main surface of a ceramic plate (1). The metal frame (13) is fastened to the external heat sink (11) by screws (12) or bonded to the external heat sink (11) with an adhesive. The flange (20) of the metal frame (13) fastened or bonded to the external heat sink (11) presses the outer peripheral part of the insulating substrate (17) toward the external heat sink (11). This pressing force holds the insulating substrate (17) in pressure contact with the external heat sink (11). The semiconductor module (18) avoids the problem of a decreasing pressing force resulting from deformation to ensure a satisfactory heat dissipating property over a long period of time.

FIG. 1

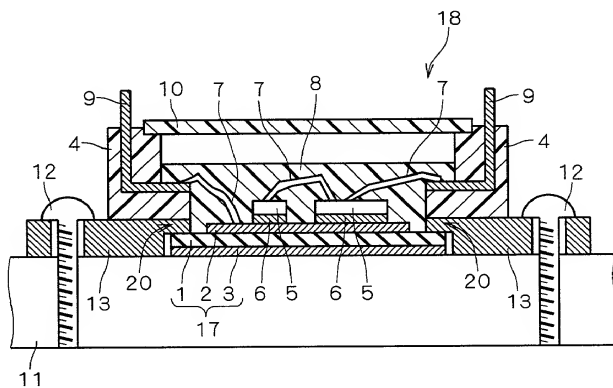


FIG. 2

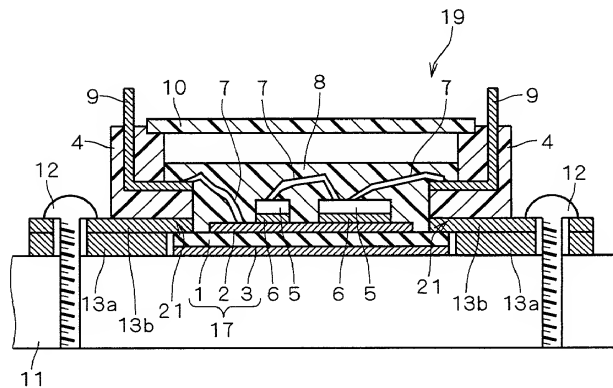


FIG. 3

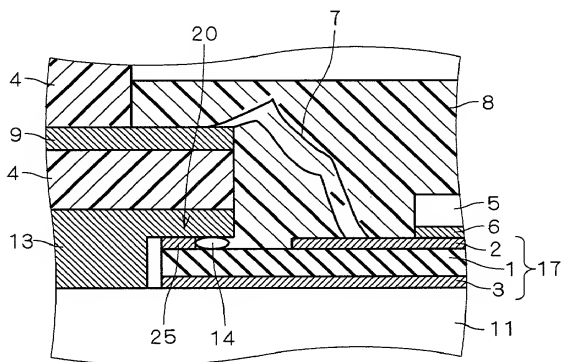


FIG. 4

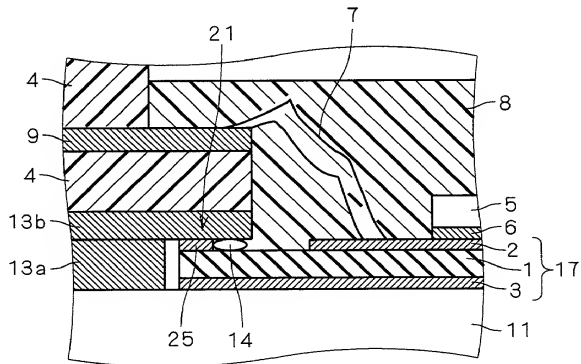


FIG. 5

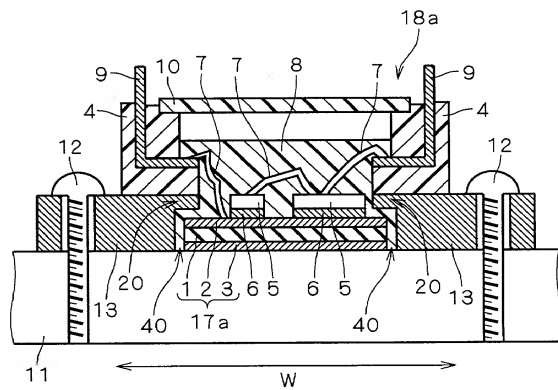


FIG. 6

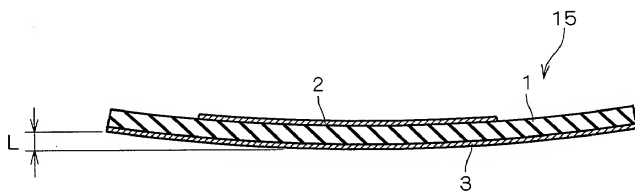


FIG. 7

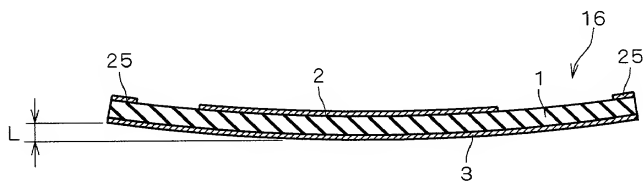


FIG. 8

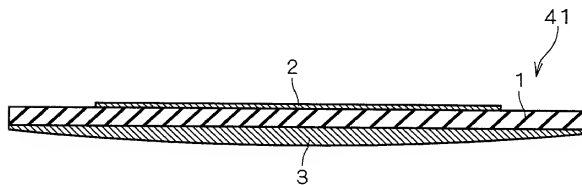
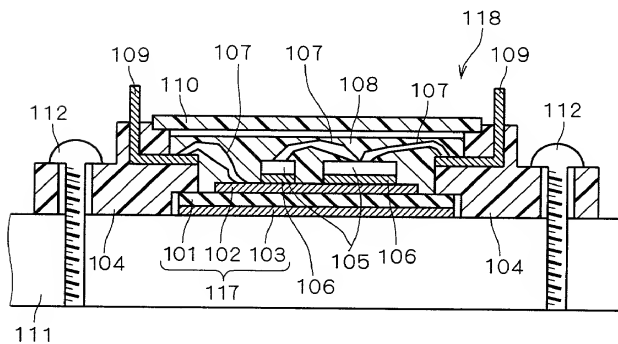


FIG. 9



Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者（下記の名称が複数の場合）であると信じています。

上記発明の明細書は、

☐ 本書に添付されています。

☐ 月 日に提出され、米出国願番号または特許協定条約国際出願番号を _____ とし、
(該当する場合) _____ に訂正されました。

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

SEMICONDUCTOR MODULE AND INSULATING

SUBSTRATE THEREOF

the specification of which

☒ is attached hereto.

☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration (日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基き下記、米国以外の国の少なくとも一か国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願

P11-305327

(Number)
(番号)

JAPAN

(Country)
(国名)

(Number)
(番号)

(Country)
(国名)

私は、第35編米国法典119条 (e) 項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条 (c) に基づく権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

私は、私自信の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づき表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行なえば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Claimed
優先権主張

<input checked="" type="checkbox"/> Yes はい	<input type="checkbox"/> No いいえ
<input type="checkbox"/> Yes はい	<input type="checkbox"/> No いいえ

October 27, 1999
(Day/Month/Year Filed)
(出願年月日)

(Day/Month/Year Filed)
(出願年月日)

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)
(出願番号)

(Filing Date)
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration
(日本語宣言書)

委任状：私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。
(弁理士、または代理人の指名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: (list name and registration number)

Norman F. Oblon, Registration Number 24,618; Marvin J. Spivak, Registration Number 24,913; C. Irvin McClelland, Registration Number 21,124; Gregory J. Maier, Registration Number 25,599; Arthur I. Neustadt, Registration Number 24,854; Richard D. Kelly, Registration Number 27,757; James D. Hamilton, Registration Number 28,421; Eckhard H. Kuesters, Registration Number 28,870; Robert T. Pous, Registration Number 29,099; Charles L. Gholz, Registration Number 26,395; Vincent J. Sunderdick, Registration Number 29,004; William E. Beaumont, Registration Number 30,996; Steven B. Keiber, Registration Number 30,073; Robert F. Gnuse, Registration Number 27,295; Jean-Paul Lavalleye, Registration Number 31,451; Stephen G. Baxter, Registration Number 32,884; Martin M. Zolnick, Registration Number 35,745; Robert W. Hahl, Registration Number 33,893; Richard L. Treanor, Registration Number 36,379; Steven P. Weihrouh, Registration Number 32,829; John T. Goolkasian, Registration Number 26,142; Marc R. Labgold, Registration Number 34,651; William J. Healey, Registration Number 36,160; Richard L. Chinn, Registration Number 34,305; Steven E. Lipman, Registration Number 30,011; Carl E. Schlier, Registration Number 34,426; James J. Kubaski, Registration Number 34,648; Catherine B. Richardson, Registration Number 39,007; Richard A. Neifeld, Registration Number 35,299; and J. Derek Mason, Registration Number 35,270; with full powers of substitution and revocation.

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発明者の署名	Inventor's signature <i>Toshiaki Shinohara</i>
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国籍	Citizenship JAPAN
私書箱	Post Office Address c/o Mitsubishi Denki Kabushiki Kaisha, 2-3, Marunouchi 2-chome, Chiyoda-ku, TOKYO 100-8310 JAPAN
第二共同発明者	Full name of second joint inventor, if any Akira FUJITA
第二共同発明者の署名	Second inventor's signature <i>Akira Fujita</i>
日付	Date March 6, 2000
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(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)

Japanese Language Declaration
(日本語宣言書)

第三の共同発明者の氏名	Full name of third joint inventor, if any Takanobu YOSHIDA	
第三の共同発明者の署名	日付	Third joint inventor's signature <i>Takanobu Yoshida</i> Date March 6, 2000
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第四の共同発明者の氏名	Full name of fourth joint inventor, if any	
第四の共同発明者の署名	日付	Fourth joint inventor's signature Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	

第五の共同発明者の氏名	Full name of fifth joint inventor, if any	
第五の共同発明者の署名	日付	Fifth joint inventor's signature Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	

第六の共同発明者の氏名	Full name of sixth joint inventor, if any	
第六の共同発明者の署名	日付	Sixth joint inventor's signature Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	